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Electronic Version v1.1

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Title of Invention

METHOD FOR OPTIMAL USE OF DIRECT FIT AND INTERPOLATED
MODELS IN SCHEMATIC CUSTOM DESIGN OF ELECTRICAL CIRCUITS

Application Number :

Date :

First Named Applicant: Joseph A. Iadanza

Confirmation Number:

Attorney Docket Number: BUR920030174US1

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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files
us-fee-sheet	BUR920030174US1-usfees.xml us-fee-sheet.xml us-fee-sheet.dtd
us-assignment	BUR920030174US1-usassn.xml us-assignment.xml us-assignment.dtd BUR920030174US1Assignment1of2.tif BUR920030174US1Assignment2of2.tif
us-request	BUR920030174US1-usrequ.xml us-request.dtd us-request.xml
us-declaration application-body	BUR920030174US1Declaration1of1.tif BUR920030174US1spec-trans.xml us-application-body.xml application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920030174US1FIGSsheet1of3.tif BUR920030174US1FIGSsheet2of3.tif BUR920030174US1FIGSsheet3of3.tif
Comments	
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